

NO. 531 P. 1/5

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FACSIMILE COVER SHEET

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
MEARS ET AL.)
)
Serial No. 10/647,069) Attorney Docket No. 62602
)
Filing Date: AUGUST 22, 2003)
)
For: SEMICONDUCTOR DEVICE INCLUDING)
MOSFET HAVING BAND-ENGINEERED)
SUPERLATTICE)
)

REQUEST FOR CORRECTION TO THE FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

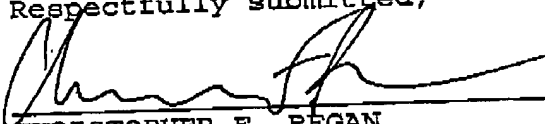
The filing receipt for the subject patent application has been received and reviewed for accuracy. Several discrepancies have been discovered. The filing receipt in error lists the name of the Assignee as "MEARS CORPORATION, Newton, MA; R.J. Mears LLC". The correct name of the Assignee is RJ Mears, LLC. The next error occurs because the address of the Assignee is not listed. The new address of the Assignee is Waltham, MA. The third error occurs because the domestic priority data is not listed. The correct data is 10/603,621 filed 6/26/03; 10/603,696 filed 6/26/03.

In view of the errors noted, it is respectfully requested that a corrected filing receipt be issued in this

In re Patent Application of:
MEARS ET AL.
Serial No. 10/647,069
Filed: AUGUST 22, 2003

application. If any fee is believed to be necessary,
authorization is given to charge Deposit Account No. 01-0484.

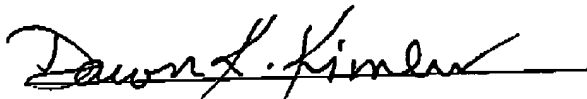
Respectfully submitted,



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CERTIFICATE OF FACSIMILE

I HEREBY CERTIFY that this REQUEST FOR CORRECTION TO
THE FILING RECEIPT for U.S. Serial No. 10/647,069 is being
submitted via telefacsimile no. 703-746-9195 to: OFFICE OF
INITIAL PATENT EXAMINATION'S FILING RECEIPT CORRECTIONS, P.O.
BOX 1450 ALEXANDRIA, VA 22313-1450, this 23rd day of March,
2004.





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| APPL NO. | FILING OR 371 (c) DATE | ART UNIT | FIL FEE REC'D | ATTY. DOCKET NO | DRAWINGS | TOT CLMS | IND CLMS |
|------------|---------------------------|----------|---------------|-----------------|----------|----------|----------|
| 10/647,069 | 08/22/2003 | 2811 | 995 | 62602 | 9 | 71 | 5 |

CONFIRMATION NO. 9700

27975

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UPDATED FILING RECEIPT

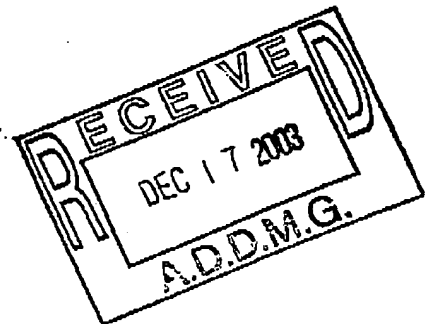
10/647,069 08/22/2003 2811 995 62602 9 71 5
OC000000011481028

Date Mailed: 12/12/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Assignment For Published Patent Application

MEARS CORPORATION, Newton, MA;
B.J. Mears, LLC; Waltham, MA
RS

Domestic Priority data as claimed by applicant

CIP of 10/603,621 06/26/03
CIP of 10/603,616 06/26/03

Foreign Applications

If Required, Foreign Filing License Granted: 11/21/2003

Projected Publication Date: 02/24/2005

Non-Publication Request: No

Early Publication Request: No

** SMALL ENTITY **

Title

Semiconductor device including mosfet having band-engineered superlattice

Preliminary Class

257

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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